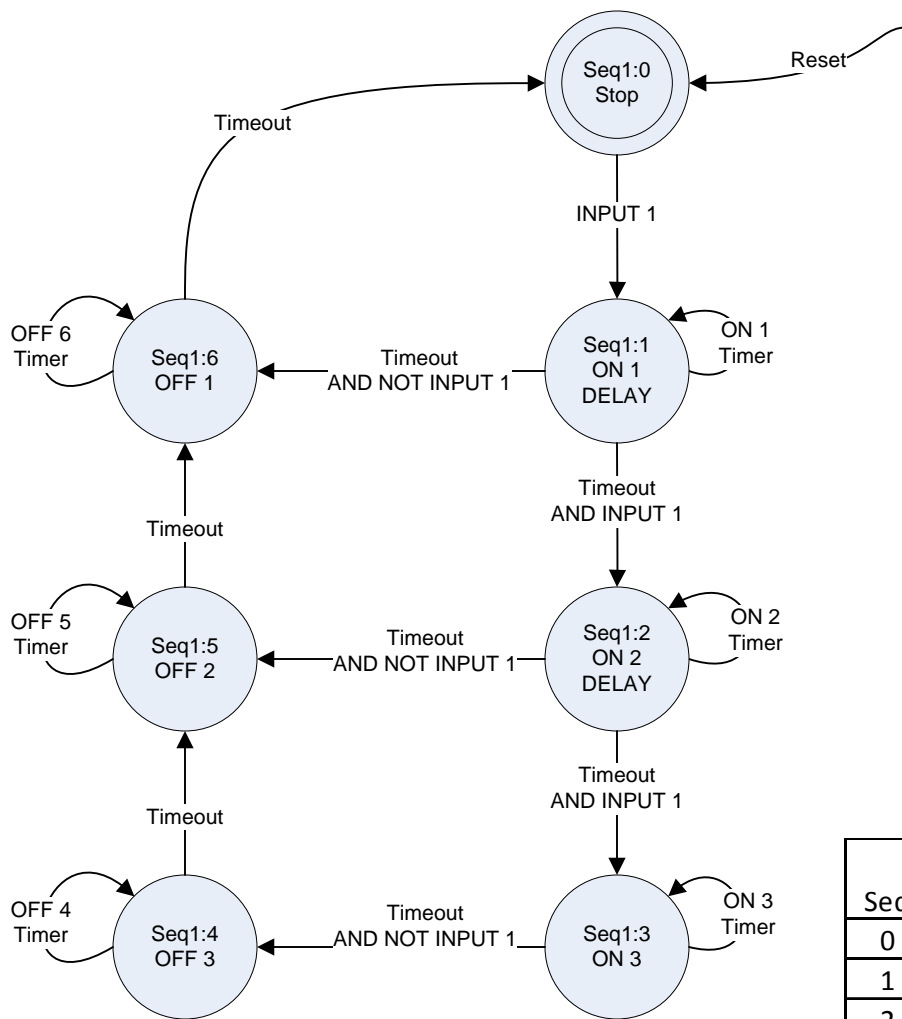


Sequential Output State Machine



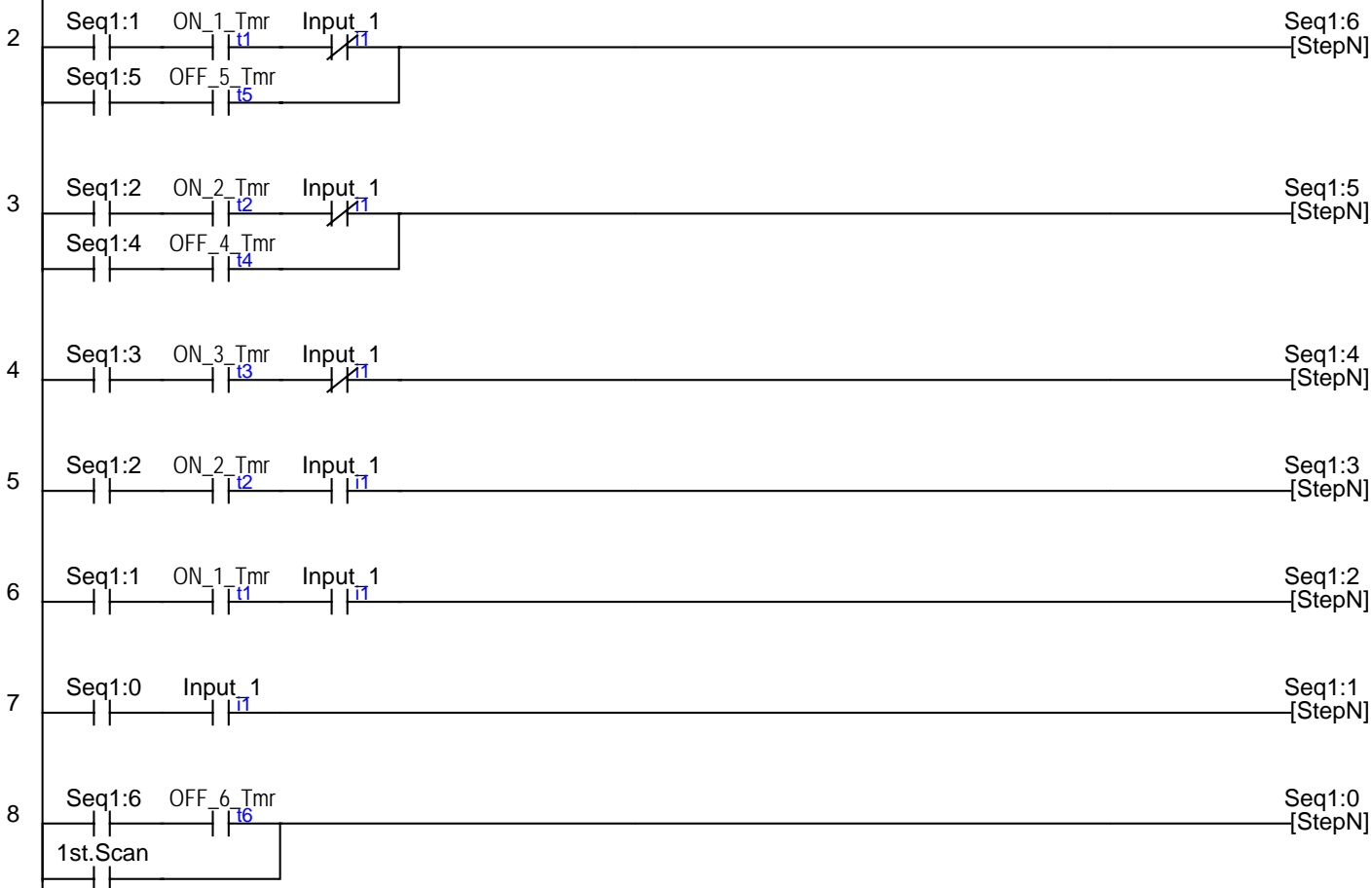
	Output	Output	Output
Seq	1	2	3
0	0	0	0
1	1	0	0
2	1	1	0
3	1	1	1
4	1	1	0
5	1	0	0
6	0	0	0

1 Main State Machine: State Transistion Table

Notes:

The transistion table is written in reverse order. I write the tables in this order as a point of disipline.

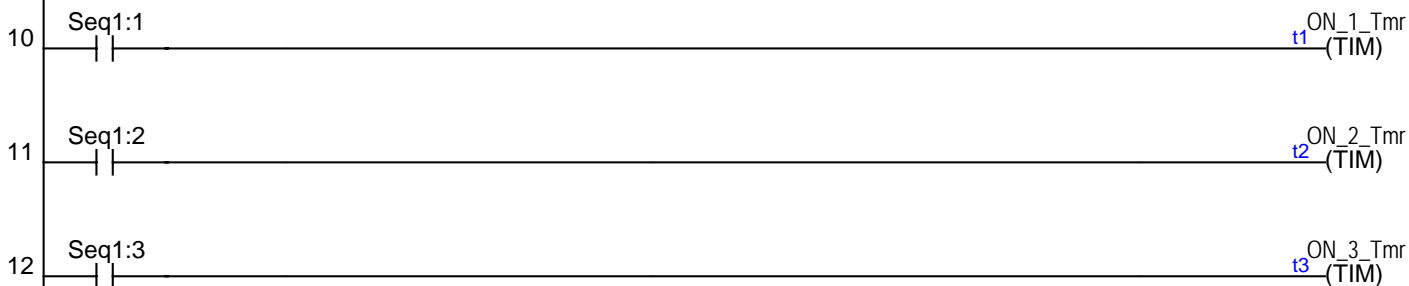
Please be careful when modifying this code to ensure that each transistion is unique and that is is impossible for the state machine to fall through more than a single state on a single scan.

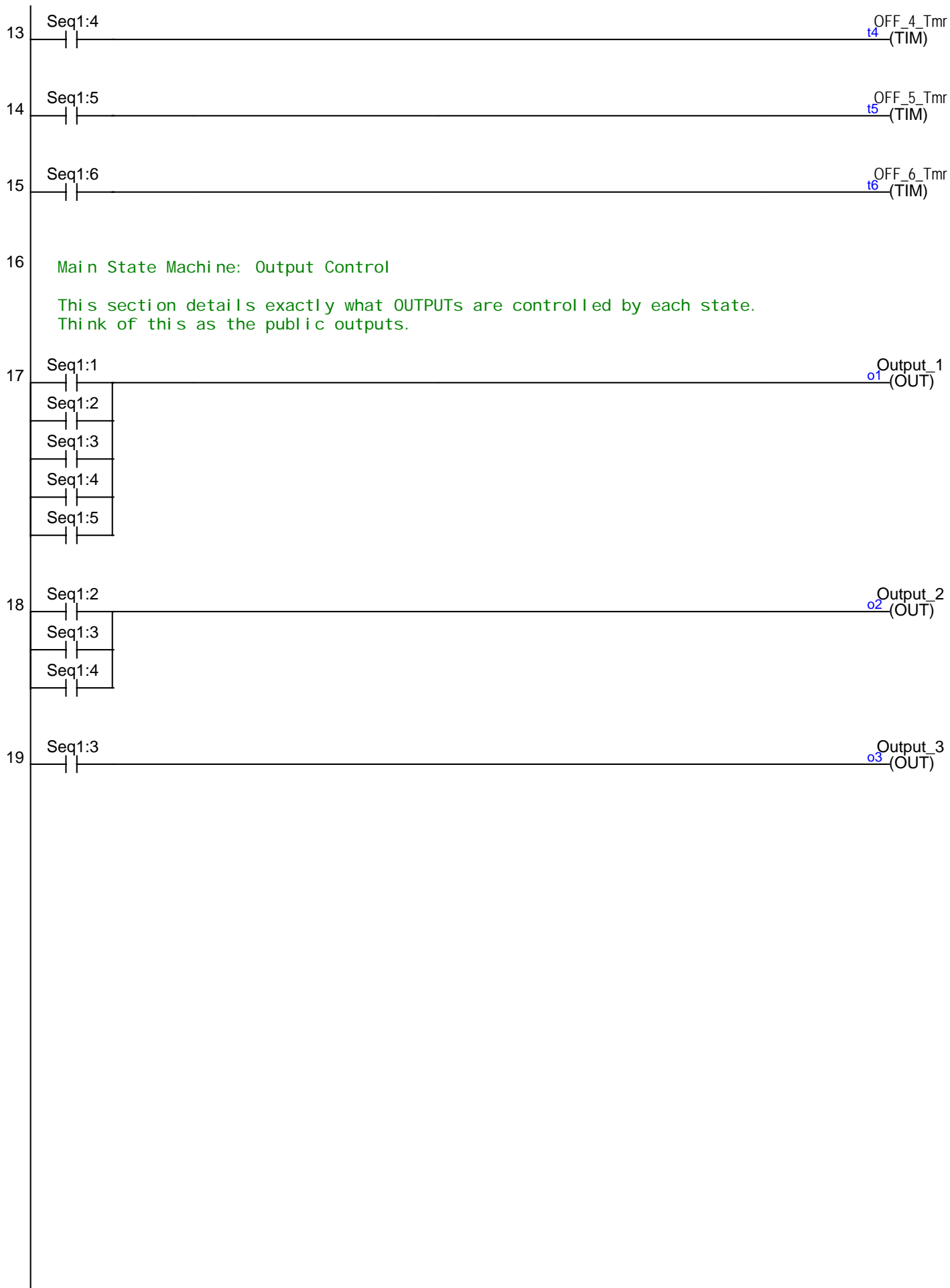


9 Main State Machine: Timer / Counter Control

This section details the TIMERS that are controlled by each state. These TIMERS are used only by the State Machine and are used to contorl when the machine is ready to change state.

These details should be considered private or internal details required to implement the state machine.





I/O #	Inputs	Outputs	Relays	Timers	S.V.	Counters	S.V.
1	Input_1	Output_1		ON_1_Tmr	25	Seq1	32
2		Output_2		ON_2_Tmr	25		
3		Output_3		ON_3_Tmr	25		
4				OFF_4_Tmr	25		
5				OFF_5_Tmr	25		
6				OFF_6_Tmr	25		
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